

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of )  
OSAMU NAKAMURA et al )  
Serial No. 10/056,055 )  
Filed: January 28, 2002 )  
For: SEMICONDUCTOR DEVICE AND )  
MANUFACTURING METHOD OF THE )  
SAME )

Group Art Unit: 2811

Examiner: Unassigned

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INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents


Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

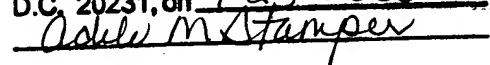
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Respectfully submitted,

  
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**CERTIFICATE OF MAILING**

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PTO/SB/08A (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>				<b>Complete if Known</b>		
				Application Number		10/056,055
				Filing Date		January 28, 2002
				First Named Inventor		Osamu NAKAMURA et al.
				Group Art Unit		2811
				Examiner Name		Unassigned
Sheet	1	of	1	Attorney Docket Number		0756-2428

U.S. PATENT DOCUMENTS						
Examiner Initials <sup>*</sup>	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			

FOREIGN PATENT DOCUMENTS								
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials <sup>*</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Kevin S. Jones et al., "Boron Diffusion Upon Annealing of Laser Thermal Processed Silicon," Ion Implantation Technology 2000, Conference on, 2000, pages 111-114	
		D.J. Llewellyn et al., "Implantation and Annealing of Cu in InP for Electrical Isolation: Microstructural Characterisation," Optoelectronic and Microelectronic Materials and Devices Proceedings, 1996, pages 313-316	

Examiner Signature		Date Considered	
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